

[Time:2.30 Hrs]		[Marks:75]
Please check whether you have got the right question paper.		
N.B:	1. All question are compulsory. 2. Figures to the right indicate full marks. 3. Students answering in the regional language should refer in case of doubt to the main text of the paper in English.	

Q.1	Attempt any three of the following: a. Explain the types of computer languages. b. List and explain different types of logic devices. c. Explain architecture of 8085 Microprocessor. d. Write a short note on 1. Opcode fetch cycle 2.I/O devices e. What is a microprocessor? What is the difference between a microprocessor and CPU. f. Draw and explain pin diagram of 8085.	15
Q.2	Attempt any three of the following: a. Write a difference between a Memory-Mapped I/O and Peripheral I/O? b. Explain data transfer operations with examples. c. Explain 8085 programming model. d. Explain Logic operations with examples. e. Explain flowchart with example. f. Explain Arithmetic operations with examples.	15
Q.3	Attempt any three of the following: a. Explain the concept of Stack, subroutines, Return, Restart and conditional call. b. Write the steps for debugging counter and Timer delay programs. c. Write in short about Counter & Time Delays in 8085. d. Explain dynamic debugging in details. e. Write a program to arrange an array of descending order f. Explain Looping, Counting And Indexing.	15
Q.4	Attempt any three of the following: a. Explain BCD –to –binary conversion with examples. b. Describe about software development of microprocessor. c. Explain the working of an interrupt in 8085 microprocessor. d. Explain the concept of Operating System. e. Explain BCD Subtraction with examples. f. Write a short note on Direct Memory Access (DMA).	15
Q.5	Attempt any three of the following: a. State the comparison between Intel Core and Nehalem processors b. Explain architecture of Pentium IV. c. Explain the page tables and memory management concept in processor. d. Compare the performance of Pentium IV and i5 microprocessor. e. Explain control instructions set. f. Explain SPARC architecture.	15